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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/846,486 05/01/2001 PAT 6,736,942
 which claims benefit of 60/201,292 05/02/2000

of 99

** FOREIGN APPLICATIONS *****

none 99

IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY **

** 04/23/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY MD	SHEETS DRAWING 6	TOTAL CLAIMS 1	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's signature <i>[Signature]</i>				

ADDRESS

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TITLE

Method of connecting semiconductor or microelectronic device to a substrate

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
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		<input type="checkbox"/> 1.17 Fees (Processing Ext. of

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